

# **CLOC: Compact Low-Overhead CFB** \*

Name: CLOC v1

Designers/Submitters: Tetsu Iwata, Nagoya University, Japan  
Kazuhiko Minematsu, NEC Corporation, Japan  
Jian Guo, Nanyang Technological University, Singapore  
Sumio Morioka, NEC Europe Ltd., United Kingdom

Contact Address: Tetsu Iwata, [iwata@cse.nagoya-u.ac.jp](mailto:iwata@cse.nagoya-u.ac.jp)

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\* This document was prepared based on [14,15] and [27].

# 1 Specification

CLOC (which stands for Compact Low-Overhead CFB, and is pronounced as “clock”) is a blockcipher mode of operation for authenticated encryption with associated data (AEAD), which is also called an authenticated cipher. The design of CLOC aims at being provably secure and optimizing the implementation overhead beyond the blockcipher, the precomputation complexity, and the memory requirement. CLOC handles short input data efficiently, and is suitable for use with embedded processors.

CLOC was presented in [14], and the main difference of our CAESAR submission from is [14] that the minimum data unit is defined to be a byte (8 bits) string, and we instantiate CLOC based on AES blockcipher for 16-byte block length and TWINE blockcipher [27] for 8-byte block length.

## 1.1 Notation

Let  $\{0, 1\}^*$  be the set of all finite bit strings, including the empty string  $\varepsilon$ . For an integer  $\ell \geq 0$ , let  $\{0, 1\}^\ell$  be the set of all bit strings of  $\ell$  bits. We let  $\mathbb{B} = \{0, 1\}^8$  be the set of bytes (8-bit strings), and  $\mathbb{B}^*$  be the set of all finite byte strings. For  $X, Y \in \{0, 1\}^*$ , we write  $X \parallel Y$ ,  $(X, Y)$ , or  $XY$  to denote their concatenation. For  $\ell \geq 0$ , we write  $0^\ell \in \{0, 1\}^\ell$  to denote the bit string that consists of  $\ell$  zeros, and  $1^\ell \in \{0, 1\}^\ell$  to denote the bit string that consists of  $\ell$  ones. For  $X \in \{0, 1\}^*$ ,  $|X|$  is its length in bits, and for  $\ell \geq 1$ ,  $|X|_\ell = \lceil |X|/\ell \rceil$  is the length in  $\ell$ -bit blocks. For  $X \in \{0, 1\}^*$  and  $\ell \geq 0$  such that  $|X| \geq \ell$ ,  $\text{msb}_\ell(X)$  is the most significant (the leftmost)  $\ell$  bits of  $X$ . For instance we have  $\text{msb}_1(1100) = 1$  and  $\text{msb}_3(1100) = 110$ . For  $X \in \{0, 1\}^*$  and  $\ell \geq 1$ , we write its partition into  $\ell$ -bit blocks as  $(X[1], \dots, X[x]) \stackrel{\ell}{\leftarrow} X$ , which is defined as follows. If  $X = \varepsilon$ , then  $x = 1$  and  $X[1] \stackrel{\ell}{\leftarrow} X$ , where  $X[1] = \varepsilon$ . Otherwise  $X[1], \dots, X[x] \in \{0, 1\}^*$  are unique bit strings such that  $X[1] \parallel \dots \parallel X[x] = X$ ,  $|X[1]| = \dots = |X[x-1]| = \ell$ , and  $1 \leq |X[x]| \leq \ell$ .

In what follows, we fix a block length  $n$  and a blockcipher  $E : \mathcal{K}_E \times \{0, 1\}^n \rightarrow \{0, 1\}^n$ , where  $\mathcal{K}_E$  is a non-empty set of keys. Let  $\text{Perm}(n)$  be the set of all permutations over  $\{0, 1\}^n$ . We write  $E_K \in \text{Perm}(n)$  for the permutation specified by  $K \in \mathcal{K}_E$ , and  $C = E_K(M)$  for the ciphertext of plaintext  $M \in \{0, 1\}^n$  under key  $K \in \mathcal{K}_E$ . Following the CAESAR call for submissions, we restrict all input and output variables of CLOC as byte-strings. Also we assume the big-endian format for all variables.

## 1.2 Algorithm and Parameters

CLOC takes three parameters, a blockcipher  $E : \mathcal{K}_E \times \{0, 1\}^n \rightarrow \{0, 1\}^n$ , a nonce length  $\ell_N$ , and a tag length  $\tau$ , where  $\ell_N$  and  $\tau$  are in bits. Here, a nonce corresponds to a public message number specified by the CAESAR call for submissions, and we may interchangeably use both names. CLOC does not have the secret message number, i.e. it is always assumed to be of length zero. We require  $1 \leq \ell_N \leq n - 1$  and  $1 \leq \tau \leq n$ , and assume that  $\ell_N/8$  and  $\tau/8$  are integers, and  $n \in \{64, 128\}$ . We write  $\text{CLOC}[E, \ell_N, \tau]$  for CLOC that is parameterized by  $E$ ,  $\ell_N$ , and  $\tau$ , and we often omit the parameters if they are irrelevant or they are clear from the context.  $\text{CLOC}[E, \ell_N, \tau] = (\text{CLOC-}\mathcal{E}, \text{CLOC-}\mathcal{D})$  consists of the encryption algorithm  $\text{CLOC-}\mathcal{E}$  and the decryption algorithm  $\text{CLOC-}\mathcal{D}$ .

$\text{CLOC-}\mathcal{E}$  and  $\text{CLOC-}\mathcal{D}$  have the following syntax.

$$\begin{cases} \text{CLOC-}\mathcal{E} : \mathcal{K}_{\text{CLOC}} \times \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \times \mathcal{M}_{\text{CLOC}} \rightarrow \mathcal{CT}_{\text{CLOC}} \\ \text{CLOC-}\mathcal{D} : \mathcal{K}_{\text{CLOC}} \times \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \times \mathcal{CT}_{\text{CLOC}} \rightarrow \mathcal{M}_{\text{CLOC}} \cup \{\perp\} \end{cases}$$

$\mathcal{K}_{\text{CLOC}} = \mathcal{K}_E$  is the key space, which is identical to the key space of the underlying blockcipher,  $\mathcal{N}_{\text{CLOC}} = \mathbb{B}^{\ell_N/8}$  is the nonce space,  $\mathcal{A}_{\text{CLOC}} = \mathbb{B}^*$  is the associated data space,  $\mathcal{M}_{\text{CLOC}} = \mathbb{B}^*$  is the plaintext space,  $\mathcal{CT}_{\text{CLOC}} = \mathcal{C}_{\text{CLOC}} \times \mathcal{T}_{\text{CLOC}}$  is the ciphertext space, where  $\mathcal{C}_{\text{CLOC}} = \mathbb{B}^*$  and  $\mathcal{T}_{\text{CLOC}} = \mathbb{B}^{\tau/8}$  is the tag space, and  $\perp \notin \mathcal{M}_{\text{CLOC}}$  is the distinguished reject symbol. We write  $(C, T) \leftarrow \text{CLOC-}\mathcal{E}_K(N, A, M)$  and  $M \leftarrow \text{CLOC-}\mathcal{D}_K(N, A, C, T)$  or  $\perp \leftarrow \text{CLOC-}\mathcal{D}_K(N, A, C, T)$ .

$\text{CLOC-}\mathcal{E}$  and  $\text{CLOC-}\mathcal{D}$  are defined in Fig. 1. In these algorithms, we use four subroutines, HASH, PRF, ENC, and DEC. They have the following syntax.

$$\begin{cases} \text{HASH} : \mathcal{K}_{\text{CLOC}} \times \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \rightarrow \{0, 1\}^n \\ \text{PRF} : \mathcal{K}_{\text{CLOC}} \times \{0, 1\}^n \times \mathcal{C}_{\text{CLOC}} \rightarrow \mathcal{T}_{\text{CLOC}} \\ \text{ENC} : \mathcal{K}_{\text{CLOC}} \times \{0, 1\}^n \times \mathcal{M}_{\text{CLOC}} \rightarrow \mathcal{C}_{\text{CLOC}} \\ \text{DEC} : \mathcal{K}_{\text{CLOC}} \times \{0, 1\}^n \times \mathcal{C}_{\text{CLOC}} \rightarrow \mathcal{M}_{\text{CLOC}} \end{cases}$$

<b>Algorithm CLOC-<math>\mathcal{E}_K(N, A, M)</math></b> 1. $V \leftarrow \text{HASH}_K(N, A)$ 2. $C \leftarrow \text{ENC}_K(V, M)$ 3. $T \leftarrow \text{PRF}_K(V, C)$ 4. <b>return</b> $(C, T)$	<b>Algorithm CLOC-<math>\mathcal{D}_K(N, A, C, T)</math></b> 1. $V \leftarrow \text{HASH}_K(N, A)$ 2. $T^* \leftarrow \text{PRF}_K(V, C)$ 3. <b>if</b> $T \neq T^*$ <b>then return</b> $\perp$ 4. $M \leftarrow \text{DEC}_K(V, C)$ 5. <b>return</b> $M$
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**Fig. 1.** Pseudocode of the encryption and the decryption algorithms of CLOC

<b>Algorithm HASH<math>_K(N, A)</math></b> 1. $(A[1], \dots, A[a]) \stackrel{r}{\leftarrow} A$ 2. $S_H[1] \leftarrow E_K(\text{fix0}(\text{ozp}(A[1])))$ 3. <b>if</b> $\text{msb}_1(\text{ozp}(A[1])) = 1$ <b>then</b> 4. $S_H[1] \leftarrow h(S_H[1])$ 5. <b>if</b> $a \geq 2$ <b>then</b> 6. <b>for</b> $i \leftarrow 2$ <b>to</b> $a - 1$ <b>do</b> 7. $S_H[i] \leftarrow E_K(S_H[i - 1] \oplus A[i])$ 8. $S_H[a] \leftarrow E_K(S_H[a - 1] \oplus \text{ozp}(A[a]))$ 9. <b>if</b> $ A[a]  = n$ <b>then</b> 10. $V \leftarrow f_1(S_H[a] \oplus \text{ozp}(N))$ 11. <b>else</b> <span style="float: right;">// <math>0 \leq  A[a]  \leq n - 1</math></span> 12. $V \leftarrow f_2(S_H[a] \oplus \text{ozp}(N))$ 13. <b>return</b> $V$	<b>Algorithm PRF<math>_K(V, C)</math></b> 1. <b>if</b> $ C  = 0$ <b>then</b> 2. $T \leftarrow \text{msb}_\tau(E_K(g_1(V)))$ 3. <b>return</b> $T$ 4. $(C[1], \dots, C[m]) \stackrel{r}{\leftarrow} C$ 5. $S_P[0] \leftarrow E_K(g_2(V))$ 6. <b>for</b> $i \leftarrow 1$ <b>to</b> $m - 1$ <b>do</b> 7. $S_P[i] \leftarrow E_K(S_P[i - 1] \oplus C[i])$ 8. <b>if</b> $ C[m]  = n$ <b>then</b> 9. $S_P[m] \leftarrow E_K(f_1(S_P[m - 1] \oplus C[m]))$ 10. <b>else</b> <span style="float: right;">// <math>1 \leq  C[m]  \leq n - 1</math></span> 11. $S_P[m] \leftarrow E_K(f_2(S_P[m - 1] \oplus \text{ozp}(C[m])))$ 12. $T \leftarrow \text{msb}_\tau(S_P[m])$ 13. <b>return</b> $T$
<b>Algorithm ENC<math>_K(V, M)</math></b> 1. <b>if</b> $ M  = 0$ <b>then</b> 2. $C \leftarrow \varepsilon$ 3. <b>return</b> $C$ 4. $(M[1], \dots, M[m]) \stackrel{r}{\leftarrow} M$ 5. $S_E[1] \leftarrow E_K(V)$ 6. <b>for</b> $i \leftarrow 1$ <b>to</b> $m - 1$ <b>do</b> 7. $C[i] \leftarrow S_E[i] \oplus M[i]$ 8. $S_E[i + 1] \leftarrow E_K(\text{fix1}(C[i]))$ 9. $C[m] \leftarrow \text{msb}_{ M[m] }(S_E[m]) \oplus M[m]$ 10. $C \leftarrow (C[1], \dots, C[m])$ 11. <b>return</b> $C$	<b>Algorithm DEC<math>_K(V, C)</math></b> 1. <b>if</b> $ C  = 0$ <b>then</b> 2. $M \leftarrow \varepsilon$ 3. <b>return</b> $M$ 4. $(C[1], \dots, C[m]) \stackrel{r}{\leftarrow} C$ 5. $S_D[1] \leftarrow E_K(V)$ 6. <b>for</b> $i \leftarrow 1$ <b>to</b> $m - 1$ <b>do</b> 7. $M[i] \leftarrow S_D[i] \oplus C[i]$ 8. $S_D[i + 1] \leftarrow E_K(\text{fix1}(C[i]))$ 9. $M[m] \leftarrow \text{msb}_{ C[m] }(S_D[m]) \oplus C[m]$ 10. $M \leftarrow (M[1], \dots, M[m])$ 11. <b>return</b> $M$

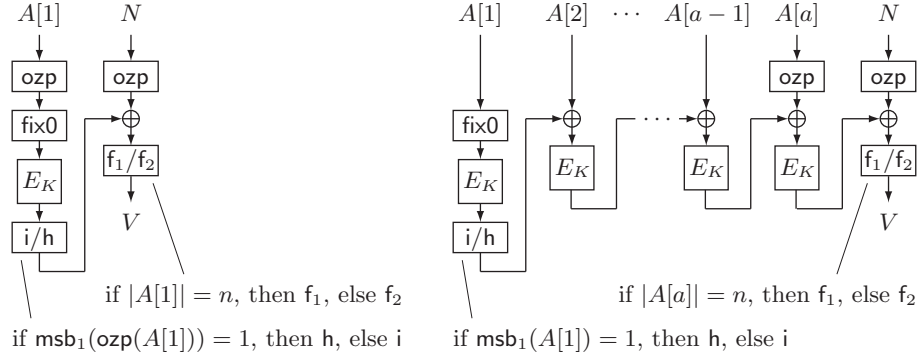
**Fig. 2.** Subroutines used in the encryption and decryption algorithms of CLOC

These subroutines are defined in Fig. 2, and illustrated in Fig. 3, Fig. 4, and Fig. 5. We also present equivalent figures in Fig. 6, Fig. 7, and Fig. 8. In the figures,  $i$  is the identity function, and  $i(X) = X$  for all  $X \in \{0, 1\}^n$ . In the subroutines, we use the one-zero padding function  $\text{ozp} : \mathbb{B}^* \rightarrow \mathbb{B}^*$ , the bit-fixing functions  $\text{fix0}, \text{fix1} : \mathbb{B}^* \rightarrow \mathbb{B}^*$ , and five tweak functions  $f_1, f_2, g_1, g_2$ , and  $h$ , which are functions over  $\{0, 1\}^n$ .

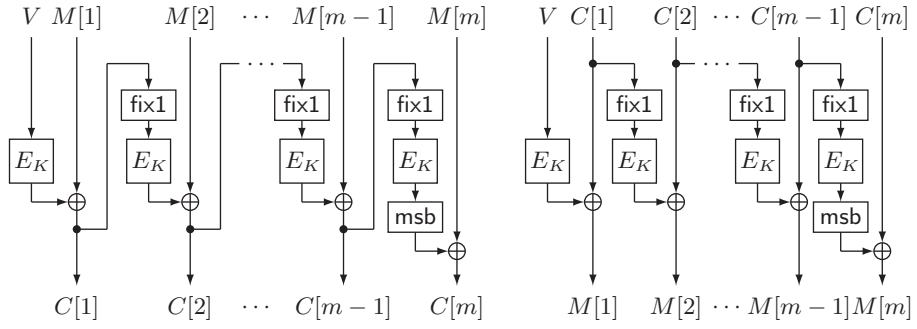
The one-zero padding function  $\text{ozp}$  is used to adjust the length of an input string so that the total length becomes a positive multiple of  $n$  bits. For  $X \in \mathbb{B}^*$ ,  $\text{ozp}(X)$  is defined as  $\text{ozp}(X) = X$  if  $|X| = \ell n$  for some  $\ell \geq 1$ , and  $\text{ozp}(X) = X \parallel 10^{n-1-(|X| \bmod n)}$  otherwise. We note that  $\text{ozp}(\varepsilon) = 10^{n-1}$ , and we also note that, in general, the function is not invertible.

The bit-fixing functions  $\text{fix0}$  and  $\text{fix1}$  are used to fix the most significant bit of an input string to zero and one, respectively. For  $X \in \mathbb{B}^*$ ,  $\text{fix0}(X)$  is defined as  $\text{fix0}(X) = X \wedge 01^{|X|-1}$ , and  $\text{fix1}(X)$  is defined as  $\text{fix1}(X) = X \vee 10^{|X|-1}$ , where  $\wedge$  and  $\vee$  are the bit-wise AND operation, and the bit-wise OR operation, respectively.

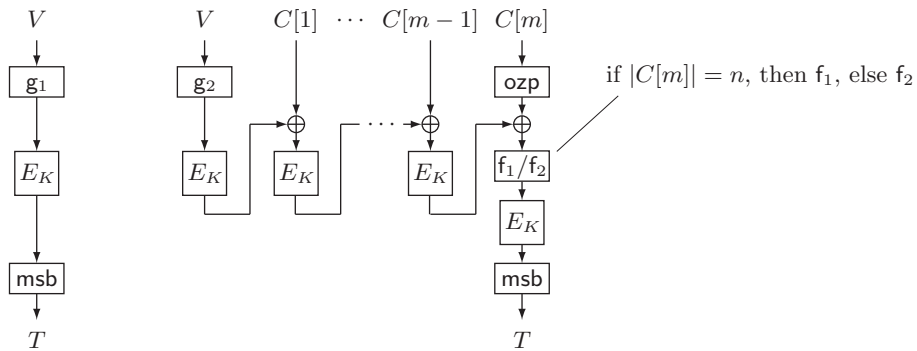
The tweak function  $h$  is used in **HASH** if the most significant bit of  $\text{ozp}(A[1])$  is one. We use  $f_1$  and  $f_2$  in **HASH** and **PRF**, where  $f_1$  is used if the last input block is full (i.e., if  $|A[a]| = n$  or  $|C[m]| = n$ ) and  $f_2$  is used otherwise. We use  $g_1$  and  $g_2$  in **PRF**, where we use  $g_1$  if the second argument of the input is the empty



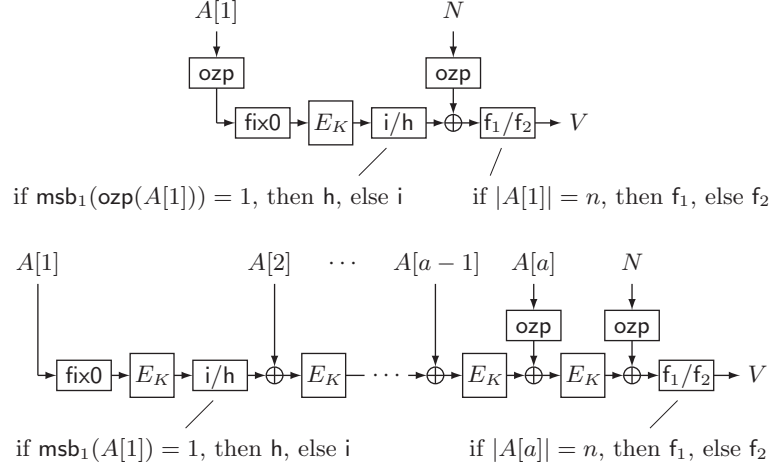
**Fig. 3.**  $V \leftarrow \text{HASH}_K(N, A)$  for  $0 \leq |A| \leq n$  (left) and  $|A| \geq n + 1$  (right)



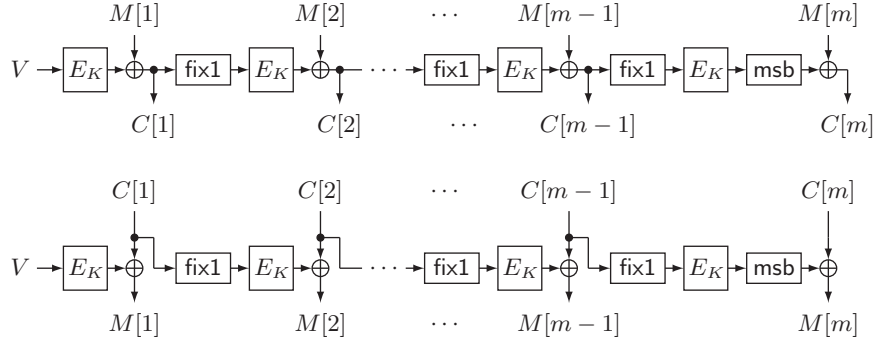
**Fig. 4.**  $C \leftarrow \text{ENC}_K(V, M)$  for  $|M| \geq 1$  (left), and  $\text{DEC}_K(V, C)$  for  $|C| \geq 1$  (right)



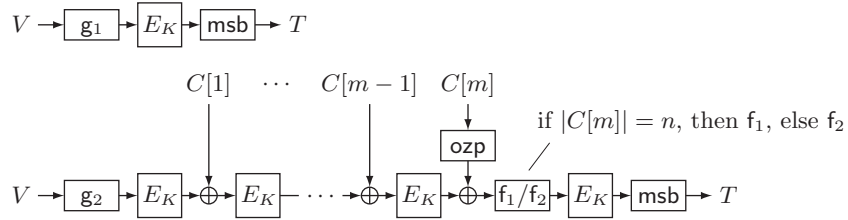
**Fig. 5.**  $T \leftarrow \text{PRF}_K(V, C)$  for  $|C| = 0$  (left) and  $|C| \geq 1$  (right)



**Fig. 6.**  $V \leftarrow \text{HASH}_K(N, A)$  for  $0 \leq |A| \leq n$  (top) and  $|A| \geq n + 1$  (bottom)



**Fig. 7.**  $C \leftarrow \text{ENC}_K(V, M)$  for  $|M| \geq 1$  (top), and  $\text{DEC}_K(V, C)$  for  $|C| \geq 1$  (bottom)



**Fig. 8.**  $T \leftarrow \text{PRF}_K(V, C)$  for  $|C| = 0$  (top) and  $|C| \geq 1$  (bottom)

string (i.e.,  $|C| = 0$ ), and otherwise we use  $\mathbf{g}_2$ . Now for  $X \in \{0, 1\}^n$ , let  $(X[1], X[2], X[3], X[4]) \stackrel{n/4}{\leftarrow} X$ . Then  $\mathbf{f}_1$ ,  $\mathbf{f}_2$ ,  $\mathbf{g}_1$ ,  $\mathbf{g}_2$ , and  $\mathbf{h}$  are defined as follows.

$$\begin{cases} \mathbf{f}_1(X) = (X[1, 3], X[2, 4], X[1, 2, 3], X[2, 3, 4]) \\ \mathbf{f}_2(X) = (X[2], X[3], X[4], X[1, 2]) \\ \mathbf{g}_1(X) = (X[3], X[4], X[1, 2], X[2, 3]) \\ \mathbf{g}_2(X) = (X[2], X[3], X[4], X[1, 2]) \\ \mathbf{h}(X) = (X[1, 2], X[2, 3], X[3, 4], X[1, 2, 4]) \end{cases}$$

Here  $X[a, b]$  stands for  $X[a] \oplus X[b]$  and  $X[a, b, c]$  stands for  $X[a] \oplus X[b] \oplus X[c]$ .

Alternatively the tweak functions can be specified by a matrix. Let

$$\mathbf{M} = \begin{pmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{pmatrix} \quad (1)$$

be a  $4 \times 4$  binary matrix, and let  $\mathbf{M}^i$  for  $i \geq 0$  be exponentiations of  $\mathbf{M}$ , where  $\mathbf{M}^0$  denotes the identity matrix. Then we have  $\mathbf{f}_1(X) = X \cdot \mathbf{M}^8$ ,  $\mathbf{f}_2(X) = X \cdot \mathbf{M}$ ,  $\mathbf{g}_1(X) = X \cdot \mathbf{M}^2$ ,  $\mathbf{g}_2(X) = X \cdot \mathbf{M}$ , and  $\mathbf{h}(X) = X \cdot \mathbf{M}^4$ , where  $X = (X[1], X[2], X[3], X[4])$  is interpreted as a vector.

### 1.3 Parameter Spaces

As the CAESAR submission we specify the parameter spaces of CLOC as follows.

- Blockcipher  $E$ : AES-128 (AES with 128-bit key), or TWINE-80 (TWINE with 80-bit key).
- Nonce length  $\ell_N$ : For AES-128,  $\ell_N \in \{8 \text{ bits (1 byte), 16 bits (2 bytes), } \dots, 120 \text{ bits (15 bytes)}\}$ , and for TWINE-80,  $\ell_N \in \{8 \text{ bits (1 byte), 16 bits (2 bytes), } \dots, 56 \text{ bits (7 bytes)}\}$ .
- Tag length  $\tau$ : For AES-128,  $\tau \in \{32 \text{ bits (4 bytes), 40 bits (5 bytes), } \dots, 128 \text{ bits (16 bytes)}\}$ , and for TWINE-80,  $\tau \in \{32 \text{ bits (4 bytes), 40 bits (5 bytes), } \dots, 64 \text{ bits (8 bytes)}\}$ .

TWINE is a 64-bit blockcipher proposed by Suzaki, Minematsu, Morioka, and Kobayashi at SAC 2012 [27]. The specification of TWINE is described in Appendix A.

### 1.4 Recommended Parameter Sets

We specify the recommended parameter sets as follows.

- Parameter set 1, `aes128n12clocv1`:  $E = \text{AES-128}$ ,  $\ell_N = 96$  (12-byte nonce),  $\tau = 64$  (8-byte tag)
- Parameter set 2, `aes128n8clocv1`:  $E = \text{AES-128}$ ,  $\ell_N = 64$  (8-byte nonce),  $\tau = 64$  (8-byte tag)
- Parameter set 3, `twine80n6clocv1`:  $E = \text{TWINE-80}$ ,  $\ell_N = 48$  (6-byte nonce),  $\tau = 32$  (4-byte tag)

## 2 Security Goals

The security goal of CLOC is to provide the provable security in terms of confidentiality (or privacy) of plaintexts under nonce-respecting adversaries, and integrity (or authenticity) of plaintext, associated data, and nonce (public message number) under nonce-reusing adversaries. That is, to keep both confidentiality and integrity, the nonce of CLOC must be unique for all encryptions, and even if this condition is violated for some reason, say by a software error, CLOC retains the authenticity of sent messages, except for replays (which can be protected by some outer mechanism). Note that CLOC has no secret message number. CLOC has provable security guarantees both for confidentiality and integrity, up to the standard birthday bound of the block length of the underlying blockcipher, based on the assumption that the blockcipher is a pseudorandom permutation (PRP). That is, for the block length of  $n$  bits, the security is guaranteed provided that the attacker obtains  $\sigma \ll 2^{n/2}$  blocks of data. A detailed explanation on the attack models and the provable security bounds are given in Sect. 3.

**Table 1.** Security goal for confidentiality (privacy)

Parameter set	aes128n12clocv1	aes128n8clocv1	twine80n6clocv1
Data	64	64	32
Time	128	128	80

**Table 2.** Security goal for integrity (authenticity)

Parameter set	aes128n12clocv1	aes128n8clocv1	twine80n6clocv1
Data	64	64	32
Verify	64	64	32
Time	128	128	80

*Attack Workload.* We provide security bounds of CLOC in Sect. 3 based on the pseudorandomness of the underlying blockcipher. We obtain Table 1 and Table 2 from these bounds. The variables in the tables denote the required workload of an adversary to break the cipher, in logarithm base 2. If one of the variables reaches the suggested number, then there is no security guarantee anymore, and the cipher can be broken. In Table 1, Data denotes  $\sigma_{\text{priv}}$  of our privacy theorem (Theorem 1), and this roughly suggests the number of data blocks that the adversary obtains. In Table 2, Data denotes  $\sigma_{\text{auth}}$  and Verify denotes  $q'$  of our authenticity theorem (Theorem 2), where  $\sigma_{\text{auth}}$  roughly suggests the number of data blocks that the adversary obtains, and  $q'$  denotes the number of decryption queries. In both tables, Time denotes the time complexity, which we assume to be equal to the bit length of the key of the underlying blockcipher. We note that small constant factors are neglected in these tables. For instance the privacy bound in Theorem 1 is  $5\sigma_{\text{priv}}^2/2^n$ , and it becomes void if  $\sigma_{\text{priv}} \approx (2^n/5)^{1/2}$ , which is slightly less than  $2^{n/2}$ .

We have already mentioned that the nonce cannot be repeated to maintain the privacy. As an additional security goal, we claim that the privacy of CLOC holds as long as the uniqueness of  $(A, N)$ , a pair of associated data and a nonce, is maintained. That is, even if the nonce is reused, if the uniqueness is maintained as the pair, then the privacy bound still holds. We note that the authenticity holds in this setting as well, since it is maintained even if the nonce is reused.

*On the Use of 64-Bit Blockcipher.* We emphasize that the use of 64-bit blockcipher, TWINE, is not for general purpose applications. The birthday bound for the block length of 64 bits is usually unacceptable for conventional data transmission. As demonstrated by McGrew [20], it leaks information when the total data blocks reach about 32 Gbytes, if the key is not renewed. However, the parameter set with 64-bit blockcipher does not focus on commodity channels, e.g., the Internet, but it focuses on networks where the data rate is significantly low, with short packet data, and sparse data transmission from edge devices. Many protocols for wireless sensor devices have a low-data rate to suppress the power consumption. For example, IEEE 802.15.4 has 20/40/250 Kbps [4], which is used as physical and data-link layers of popular sensor protocols, Zigbee and 6LoWPAN. Another example is Z-Wave, which has 9.6 or 40 Kbps [5]. If edge devices are powered by a small battery, sending 32 Gbytes for one battery is unlikely to be possible in the first place, and rekeying should occur with battery replacement. This naturally implies that the total data blocks sent from one device for its life time is small, hence it may keep the acceptable security even with a 64-bit blockcipher. For example, when the edge device sends data of 512 bytes for every thirty minutes for 10 years (which is exceptionally long for battery-powered sensor devices), the total data amount sent from the device for its life time is about 90 Mbytes. With a standard birthday bound with the block length of 64 bits, the security bound is still below  $2^{-17}$ , which can be acceptable for such constrained devices. Note that this setting assumes only one key, and if the device can renew the key, say, for each year, the bound can be reduced to  $2^{-23}$ .

Of course if the target network has a high-data rate with stable power source, we recommend to use the parameter sets with a 128-bit blockcipher.

### 3 Security Analysis

In this section, we define the security notions of a blockcipher and CLOC, and present our security theorems. The following descriptions are taken from [14], and they hold for all recommended parameter sets.

*PRP Notion.* We assume that the blockcipher  $E : \mathcal{K}_E \times \{0, 1\}^n \rightarrow \{0, 1\}^n$  is a pseudorandom permutation, or a PRP [18]. We say that  $P$  is a random permutation if  $P \stackrel{\$}{\leftarrow} \text{Perm}(n)$ , and define

$$\mathbf{Adv}_E^{\text{prp}}(\mathcal{A}) \stackrel{\text{def}}{=} \Pr \left[ \mathcal{A}^{E_K(\cdot)} \Rightarrow 1 \right] - \Pr \left[ \mathcal{A}^{P(\cdot)} \Rightarrow 1 \right],$$

where the first probability is taken over  $K \stackrel{\$}{\leftarrow} \mathcal{K}_E$  and the randomness of  $\mathcal{A}$ , and the last is over  $P \stackrel{\$}{\leftarrow} \text{Perm}(n)$  and  $\mathcal{A}$ . We write  $\text{CLOC}[\text{Perm}(n), \ell_N, \tau]$  for CLOC that uses  $P$  as  $E_K$ , and the encryption and decryption algorithms are written as  $\text{CLOC-}\mathcal{E}_P$  and  $\text{CLOC-}\mathcal{D}_P$ .

*Privacy Notion.* We define the privacy notion for  $\text{CLOC}[E, \ell_N, \tau] = (\text{CLOC-}\mathcal{E}, \text{CLOC-}\mathcal{D})$ . This notion captures the indistinguishability of a nonce-respecting adversary in a chosen plaintext attack setting. We consider an adversary  $\mathcal{A}$  that has access to the CLOC encryption oracle, or a random-bits oracle. The encryption oracle takes  $(N, A, M) \in \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \times \mathcal{M}_{\text{CLOC}}$  as input and returns  $(C, T) \leftarrow \text{CLOC-}\mathcal{E}_K(N, A, M)$ . The random-bits oracle,  $\mathcal{S}$ -oracle, takes  $(N, A, M) \in \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \times \mathcal{M}_{\text{CLOC}}$  as input and returns a random string  $(C, T) \stackrel{\$}{\leftarrow} \{0, 1\}^{|M|+\tau}$ . We define the privacy advantage as

$$\mathbf{Adv}_{\text{CLOC}[E, \ell_N, \tau]}^{\text{priv}}(\mathcal{A}) \stackrel{\text{def}}{=} \Pr \left[ \mathcal{A}^{\text{CLOC-}\mathcal{E}_K(\cdot, \cdot, \cdot)} \Rightarrow 1 \right] - \Pr \left[ \mathcal{A}^{\mathcal{S}(\cdot, \cdot, \cdot)} \Rightarrow 1 \right],$$

where the first probability is taken over  $K \stackrel{\$}{\leftarrow} \mathcal{K}_{\text{CLOC}}$  and the randomness of  $\mathcal{A}$ , and the last is over the random-bits oracle and  $\mathcal{A}$ . We assume that  $\mathcal{A}$  in the privacy game is nonce-respecting, that is,  $\mathcal{A}$  does not make two queries with the same nonce.

*Privacy Theorem.* Let  $\mathcal{A}$  be an adversary that makes  $q$  queries, and suppose that the queries are  $(N_1, A_1, M_1), \dots, (N_q, A_q, M_q)$ . Then we define the total associated data length as  $a_1 + \dots + a_q$ , and the total plaintext length as  $m_1 + \dots + m_q$ , where  $(A_i[1], \dots, A_i[a_i]) \stackrel{r}{\leftarrow} A_i$  and  $(M_i[1], \dots, M_i[m_i]) \stackrel{r}{\leftarrow} M_i$ . We have the following information theoretic result.

**Theorem 1.** *Let  $\text{Perm}(n)$ ,  $\ell_N$ , and  $\tau$  be the parameters of CLOC. Let  $\mathcal{A}$  be an adversary that makes at most  $q$  queries, where the total associated data length is at most  $\sigma_A$ , and the total plaintext length is at most  $\sigma_M$ . Then we have  $\mathbf{Adv}_{\text{CLOC}[\text{Perm}(n), \ell_N, \tau]}^{\text{priv}}(\mathcal{A}) \leq 5\sigma_{\text{priv}}^2/2^n$ , where  $\sigma_{\text{priv}} = q + \sigma_A + 2\sigma_M$ .*

A complete proof is presented in [14, Appendix A]. If we use a blockcipher  $E$ , which is secure in the sense of the PRP notion, instead of  $\text{Perm}(n)$ , then the corresponding complexity theoretic result can be shown by a standard argument. See e.g. [8].

We note that, in general, the privacy of CLOC is broken if the nonce is reused. However, as long as  $(N_i, A_i) \neq (N_j, A_j)$  holds for all  $1 \leq i < j \leq q$ , the bound in Theorem 1 holds. This is because, in the proof in [14, Appendix A], the transition from CLOC to CLOC5 works without the nonce-respecting assumption, and HASH5 and PRF5 used in CLOC5 generate random and independent output values if  $(N_i, A_i) \neq (N_j, A_j)$  holds for all  $1 \leq i < j \leq q$ .

*Authenticity Notion.* We next define the authenticity notion, which captures the unforgeability of an adversary in a chosen ciphertext attack setting. We consider a strong adversary that can repeat the same nonce multiple times. Let  $\mathcal{A}$  be an adversary that has access to the CLOC encryption oracle and the CLOC decryption oracle. The encryption oracle is defined as above. The decryption oracle takes  $(N, A, C, T) \in \mathcal{N}_{\text{CLOC}} \times \mathcal{A}_{\text{CLOC}} \times \mathcal{C}_{\text{CLOC}} \times \mathcal{T}_{\text{CLOC}}$  as input and returns  $M \leftarrow \text{CLOC-}\mathcal{D}_K(N, A, C, T)$  or  $\perp \leftarrow \text{CLOC-}\mathcal{D}_K(N, A, C, T)$ . The authenticity advantage is defined as

$$\mathbf{Adv}_{\text{CLOC}[E, \ell_N, \tau]}^{\text{auth}}(\mathcal{A}) \stackrel{\text{def}}{=} \Pr \left[ \mathcal{A}^{\text{CLOC-}\mathcal{E}_K(\cdot, \cdot, \cdot), \text{CLOC-}\mathcal{D}_K(\cdot, \cdot, \cdot)} \text{ forges} \right],$$

where the probability is taken over  $K \stackrel{\$}{\leftarrow} \mathcal{K}_{\text{CLOC}}$  and the randomness of  $\mathcal{A}$ , and the adversary forges if the decryption oracle returns a bit string (other than  $\perp$ ) for a query  $(N, A, C, T)$ , but  $(C, T)$  was not previously returned to  $\mathcal{A}$  from the encryption oracle for a query  $(N, A, M)$ . The adversary  $\mathcal{A}$  in the authenticity game is not necessarily nonce-respecting, and  $\mathcal{A}$  can make two or more queries with the same nonce. Specifically,  $\mathcal{A}$  can repeat using the same nonce for encryption queries, a nonce used for encryption queries can be used for decryption queries and vice-versa, and the same nonce can be repeated for decryption queries. Without loss of generality, we assume that  $\mathcal{A}$  does not make trivial queries, i.e., if the encryption oracle returns  $(C, T)$  for a query  $(N, A, M)$ , then  $\mathcal{A}$  does not make a query  $(N, A, C, T)$  to the decryption oracle, and  $\mathcal{A}$  does not repeat a query.



*Authenticity Theorem.* Let  $\mathcal{A}$  be an adversary that makes  $q$  encryption queries and  $q'$  decryption queries. Let  $(N_1, A_1, M_1), \dots, (N_q, A_q, M_q)$  be the encryption queries, and  $(N'_1, A'_1, C'_1, T'_1), \dots, (N'_{q'}, A'_{q'}, C'_{q'}, T'_{q'})$  be the decryption queries. Then we define the total associated data length in encryption queries as  $a_1 + \dots + a_q$ , the total plaintext length as  $m_1 + \dots + m_q$ , the total associated data length in decryption queries as  $a'_1 + \dots + a'_{q'}$ , and the total ciphertext length as  $m'_1 + \dots + m'_{q'}$ , where  $(A_i[1], \dots, A_i[a_i]) \stackrel{r}{\leftarrow} A_i$ ,  $(M_i[1], \dots, M_i[m_i]) \stackrel{r}{\leftarrow} M_i$ ,  $(A'_i[1], \dots, A'_i[a'_i]) \stackrel{r}{\leftarrow} A'_i$ , and  $(C'_i[1], \dots, C'_i[m'_i]) \stackrel{r}{\leftarrow} C'_i$ . We have the following information theoretic result.

**Theorem 2.** Let  $\text{Perm}(n)$ ,  $\ell_N$ , and  $\tau$  be the parameters of CLOC. Let  $\mathcal{A}$  be an adversary that makes at most  $q$  encryption queries and at most  $q'$  decryption queries, where the total associated data length in encryption queries is at most  $\sigma_A$ , the total plaintext length is at most  $\sigma_M$ , the total associated data length in decryption queries is at most  $\sigma_{A'}$ , and the total ciphertext length is at most  $\sigma_{C'}$ . Then we have  $\text{Adv}_{\text{CLOC}[\text{Perm}(n), \ell_N, \tau]}^{\text{auth}}(\mathcal{A}) \leq 5\sigma_{\text{auth}}^2/2^n + q'/2^\tau$ , where  $\sigma_{\text{auth}} = q + \sigma_A + 2\sigma_M + q' + \sigma_{A'} + \sigma_{C'}$ .

A complete proof is presented in [14, Appendix A]. As in the privacy case, if we use a blockcipher  $E$  secure in the sense of the PRP notion, then we obtain the corresponding complexity theoretic result by a standard argument in, e.g., [8].

## 4 Features

CLOC has the following features.

1. It uses only the encryption of the blockcipher both for encryption and decryption, and does not use bit-wise operations, such as a constant multiplication over  $\text{GF}(2^n)$ .
2. It makes  $\lceil |N|/n \rceil + \lceil |A|/n \rceil + 2\lceil |M|/n \rceil$  blockcipher calls for a nonce  $N$ , associated data  $A$ , and a plaintext  $M$ , when  $|A| \geq 1$ . No precomputation other than the blockcipher key scheduling is needed. We note that in CLOC,  $1 \leq |N| \leq n-1$  holds (hence we always have  $\lceil |N|/n \rceil = 1$ ), and when  $|A| = 0$ , it needs  $\lceil |N|/n \rceil + 1 + 2\lceil |M|/n \rceil$  blockcipher calls.
3. It works with two state blocks (i.e.  $2n$  bits).
4. Both encryption and decryption can be processed in an online manner.
5. Static associated data can be processed efficiently if the corresponding intermediate state value is stored.
6. For security, the privacy and authenticity are proved based on the PRP assumption of the blockcipher, assuming standard nonce-respecting adversaries. Moreover, the authenticity is proved with even stronger, nonce-reusing adversaries.

The second feature implies that a number of blockcipher calls required for processing short input data, say 16 or 32 bytes, is small. In particular, CLOC works without any precomputation of the blockcipher, say, computation of  $E_K(0^n)$ . The precomputation of CLOC is essentially the blockcipher key schedule, hence it can efficiently handle short input data even without precomputation. This feature is particularly desirable for low-power sensor networks, where messages are typically quite short and the devices have limited computational power. CLOC is designed to use in embedded processors, but this feature may also be useful for powerful processors, for example when the key is frequently changed or when a large number of keys need to be processed. For example, when the input data consists of 1-block nonce, 1-block associated data, and 1-block plaintext, CLOC needs 4 blockcipher calls, while we need 5 or 6 calls in CCM [12], 7 calls (where 3 out of 7 can be precomputed) in EAX [9], and 5 calls (where 1 out of 5 can be precomputed) in EAX-prime [7], where the last one is insecure [22].

The first and the third features imply that CLOC works with small memory and its efficiency for processors with small words (say 8 or 16 bits). With these features CLOC is particularly suitable for embedded processors with severe ROM/RAM constraints. The last feature implies that CLOC provides standard security as a nonce-based AEAD, and in addition a level of security (i.e. authenticity only) even when the nonce is reused, unlike many previous nonce-based AEADs.

*Advantages over AES-GCM.* Compared with AES-GCM [21], CLOC works efficiently for short input data on embedded processors, and the implementation of CLOC with AES can be smaller, as we do not use the full GF multiplier. In particular, AES-GCM is generally inefficient on embedded processors, since the GF multiplier is not fast (e.g. see [13]), while CLOC with AES can be efficiently implemented. For

CLOC with TWINE, we expect even smaller implementation, at the cost of reduced security, which will be useful for ultimately tiny processors.

With respect to the security, the provable security bound of CLOC for authentication is better, since the bound of GCM has a term  $q'(\ell_A + 1)/2^\tau$ , which grows linearly with the block length  $\ell_A$  of the associated data [16], while the corresponding term in CLOC is  $q'/2^\tau$ . This may have impact when  $\tau$  is small. Furthermore, in GCM, the existence of weak keys was pointed out [24], while weak keys are not known in CLOC. Also, CLOC provides some level of security even if the nonce is reused.

*Justifications of Parameter Sets.* For the 128-bit blockcipher, we select AES for its excellent performance and extensively studied security. For the 64-bit blockcipher, we select TWINE for its suitability for embedded processors (comparable speed to AES, smaller code size), and good performance even for high-end platforms with SIMD operations [27]. Notably, TWINE allows efficient processing of two blocks in parallel for a wide range of platforms, which is desirable for CLOC, since in CLOC, the encryption process and tag generation can be done in parallel.

For `aes128n12clocv1`, we select  $\ell_N = 96$  from the current trend on the length of the nonce, and this is suitable, for instance, if a part of the nonce is randomly chosen and the other part consists of a counter. For `aes128n8clocv1`, we select  $\ell_N = 64$  considering the data overhead, and this is suitable for applications where the nonce consists of a counter. For `twine80n6clocv1`, we select  $\ell_N = 48$  by taking the half of 96 in `aes128n12clocv1`. For all cases, the tag length was chosen by taking the balance between the security and the data overhead.

*Limitations.* We also list several limitations of CLOC. For long input data, CLOC is not efficient as it needs two blockcipher calls per one plaintext block. The nonce length is fixed, which may be problematic in some applications. The four functions used in CLOC, HASH, ENC, DEC, and HASH, are all sequential. However, the blockcipher calls in ENC and PRF can be done in parallel. We also note that the parallelization is always possible for multiple messages [11,10]. Due to the existence of five tweak functions, the hardware implementation of CLOC does not show the smallest size compared to existing schemes, since the implementation of tweak functions requires many selectors.

## 5 Design Rationale

The designers have not hidden any weaknesses in this cipher. The design rationale of CLOC is detailed in [14], and we repeat the rationale below.

Our goal is to provide an AEAD particularly efficient for processing short input data, while minimizing the memory consumption and precomputation outside the blockcipher. We mainly focus on constrained sensor networks, where each data packet is short. For example, Zigbee [6] limits the maximum packet length to 127 bytes, Bluetooth low energy limits to 47 bytes [1], and many previous proposals on sensor network security protocols, e.g., TinySec [17], defined similar limits, around 30 to 128 bytes. Another example is EPC tag, which is a replacement of bar-code using RFID and has typically 96 bits [2]. We here describe the design rationale of CLOC for achieving our goal.

At abstract level CLOC is a straightforward combination of CFB and CBC MAC, where CBC MAC is called twice for processing associated data and a ciphertext, and CFB is called once to generate a ciphertext. However, when we want to achieve low-overhead computation and small memory consumption, we found that any other combination of a basic encryption mode and a MAC mode did not work. For instance, we could not use CTR or OFB, as they require one state block in processing a plaintext to hold a counter value or a blockcipher output. We then realized that combining CFB and CBC MAC was not an easy task. Since we avoid using two keys or using blockcipher pre-calls, such as  $L = E_K(0^n)$  used in EAX, we could not computationally separate CFB and CBC MAC via input masking, such as Galois-field doubling ( $2^i L$  for the  $i$ -th block, where  $2L$  denotes the multiplication of 2 and  $L$  in  $\text{GF}(2^n)$ ) [9,25]. This implies that CFB leaks input and output pairs of the blockcipher calls, which can be freely used to guess or fake the internal chaining value of CBC MAC, leading to a break of the scheme. Lucks [19] proposed an AEAD scheme based on CFB, called CCFB. However, the problem is not relevant to CCFB due to the difference in the global structure. To overcome this obstacle in composition, we introduced the bit-fixing functions. Their role is to absolutely separate the input blocks of CFB and *the first input block* of CBC MAC. This imposes the most significant one bit of the input of CBC MAC being fixed to 0, implying one-bit input loss. The set of five tweak functions is used to compensate for this information loss. It also

$i \oplus f_1$	$i \oplus f_2h$	$f_1 \oplus f_2h$	$h \oplus g_2f_1$	$g_2f_1 \oplus g_1f_2h$
$i \oplus g_1f_1$	$i \oplus h$	$f_2 \oplus g_1f_1$	$h \oplus f_2$	$g_2f_1 \oplus f_2h$
$i \oplus g_1f_1h$	$i \oplus g_1$	$f_2 \oplus g_1f_1h$	$h \oplus g_1f_2$	$g_1f_2 \oplus g_2f_1$
$i \oplus g_2f_1$	$i \oplus g_2$	$f_2 \oplus g_2f_1$	$h \oplus g_2f_2$	$g_1f_2 \oplus g_2f_1h$
$i \oplus g_2f_1h$	$f_1 \oplus g_1f_1h$	$f_2 \oplus g_2f_1h$	$g_1f_1 \oplus f_1h$	$g_1f_2 \oplus f_1h$
$i \oplus f_1h$	$f_1 \oplus g_2f_1h$	$f_2 \oplus f_1h$	$g_1f_1 \oplus g_2f_1h$	$g_1f_2 \oplus g_2f_2h$
$i \oplus f_2$	$f_1 \oplus f_2$	$f_2 \oplus g_1f_2h$	$g_1f_1 \oplus g_2f_2$	$g_1f_2 \oplus f_2h$
$i \oplus g_1f_2$	$f_1 \oplus g_1f_2$	$f_2 \oplus g_2f_2h$	$g_1f_1 \oplus g_2f_2h$	$g_2f_2 \oplus g_1f_1h$
$i \oplus g_1f_2h$	$f_1 \oplus g_1f_2h$	$g_1 \oplus g_2$	$g_1f_1 \oplus f_2h$	$g_2f_2 \oplus f_1h$
$i \oplus g_2f_2$	$f_1 \oplus g_2f_2$	$h \oplus f_1$	$g_2f_1 \oplus g_1f_1h$	$g_2f_2 \oplus g_1f_2h$
$i \oplus g_2f_2h$	$f_1 \oplus g_2f_2h$	$h \oplus g_1f_1$	$g_2f_1 \oplus f_1h$	$g_2f_2 \oplus f_2h$

**Fig. 9.** Differential probability constraints of  $f_1, f_2, g_1, g_2,$  and  $h$

works to compensate the information loss caused by padding functions applied to the last input block to CBC MAC. A similar technique can be found in literature [23,28], however, the previous works only considered MACs and the tweak functions required bit operations.

In the following we explain the specific requirements for the tweak functions.

*Definition of  $f_1, f_2, g_1, g_2,$  and  $h$ .* These functions are defined to meet the following properties. First, they have the additive property. That is, for any  $z \in \{f_1, f_2, g_1, g_2, h\}$ , we have  $z(X \oplus X') = z(X) \oplus z(X')$  for all  $X, X' \in \{0, 1\}^n$ . Next, these functions are invertible over  $\{0, 1\}^n$ . For any  $z \in \{f_1, f_2, g_1, g_2, h\}$ , we have  $z \in \text{Perm}(n)$ . Finally, they satisfy the differential probability constraints specified in Fig. 9. Let  $z$  be a function in Fig. 9. Then we require that, for any  $Y \in \{0, 1\}^n$ ,  $\Pr[z(K) = Y] = 1/2^n$ , where the probability is taken over  $K \xleftarrow{\$} \{0, 1\}^n$ . When  $z$  is of the form  $z = z' \oplus z''$ , then  $z(K)$  stands for  $z'(K) \oplus z''(K)$ . When  $z$  is of the form  $z = z'z''$ , then  $z(K)$  stands for  $z'(z''(K))$ . Recall that we define  $i$  as  $i(K) = K$ .

*Choosing Tweak Functions.* Finding simple and word-wise tweak functions fulfilling all properties is not a trivial task. We start with matrix  $\mathbf{M}$  of (1), which is invertible and has order 15 (i.e.  $\mathbf{M}^{15} = \mathbf{M}^0$ ), and test all combinations of the form  $(f_1, f_2, g_1, g_2, h) = (i_1, \dots, i_5) \in \{1, \dots, 14\}^5$ , where  $i_1 = 2$  means  $f_1(X) = X \cdot \mathbf{M}^2$ , using a computer. There are 864 candidates out of 537,824 fulfilling the differential probability constraints of Fig. 9. The complexity increases as the index of  $\mathbf{M}$  grows, when we implement the tweak function by iterating  $\mathbf{M}$ , which seems suitable for hardware. For software we would directly implement  $\mathbf{M}^i$  using a word-wise permutation and xor, and in this case we observe slight irregular, but similar phenomena (e.g.  $\mathbf{M}^1$  needs one xor while  $\mathbf{M}^3$  needs three xor's). Fig. 10 shows  $\mathbf{M}^i$  and the Feistel-like implementations using a word-wise permutation and xor. It shows that, except for  $\mathbf{M}^5$  and  $\mathbf{M}^{10}$ , we have a simple implementation using at most four xor's. Based on these observations, we simply define the cost of computing  $\mathbf{M}^i$  as  $i$  for  $1 \leq i \leq 7$  and  $15 - i$  for  $8 \leq i \leq 14$ . Then we define  $f_{\text{cost}}(i_1, \dots, i_5)$  as

$$\left( i_1 \times \frac{1}{16} + i_2 \times \frac{15}{16} \right) \times 2 + i_4 + i_5 \times \frac{1}{2}.$$

This corresponds to the expected total cost for given  $(i_1, \dots, i_5)$ , where associated data and a plaintext are assumed to be non-empty byte strings of random lengths, (as we expect the standard use of CLOC is AEAD, not MAC), and the most significant bit of the associated data is assumed to be random. Then there remains only two candidates giving the minimum value of  $f_{\text{cost}}$ , which are  $(i_1, \dots, i_5) = (8, 1, 2, 1, 4)$  and  $(8, 1, 6, 1, 4)$ . As smaller  $i_3$  is better, we choose the former as the sole winner. We also tested other matrices, say the one replacing the forth column of  $\mathbf{M}$  by the transposition of  $(1, 0, 1, 0)$ , but no better solution was found.

We note that  $\mathbf{M}^8 = \mathbf{M}^2 \oplus \mathbf{M}^0$  and  $\mathbf{M}^4 = \mathbf{M}^1 \oplus \mathbf{M}^0$  hold, implying that we have  $f_1(X) = g_1(X) \oplus X$  and  $h(X) = f_2(X) \oplus X = g_2(X) \oplus X$ , which may be useful in some implementations.

*Selection of Blockciphers.* For  $n = 128$ , we choose AES as the underlying blockcipher, because the security of AES has been extensively studied. For  $n = 64$ , we choose TWINE as the underlying blockcipher, because of its low-resource implementation for embedded processors shown in [27]. The use of 64-bit blockcipher is particularly useful if input length is quite short, say a few bytes, which is in fact possible for ultimately constrained, single-purpose sensors such as energy harvester devices like [3], gas or water

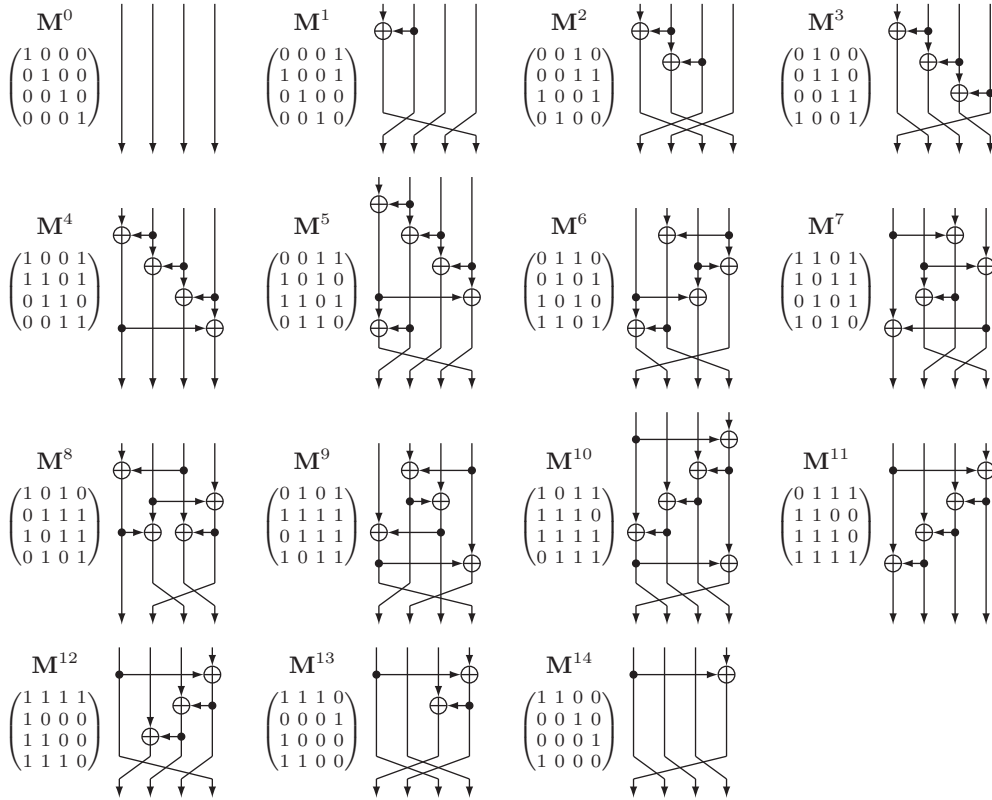


Fig. 10. Matrix exponentiations for the tweak functions

metering, etc. In such cases, a 128-bit blockcipher can be inefficient, since it is likely that we have more redundant output bits from the blockcipher that has to be discarded.

## 6 Intellectual Property

We claim no intellectual property (IP) rights associated to CLOC, and are unaware of any relevant IP held by others. We note that the statement does not cover the internal blockcipher. NEC Corporation has pending patents related to TWINE blockcipher: WO2011052585 and WO2011052587.

If any of this information changes, the submitter will promptly (and within at most one month) announce these changes on the `crypto-competitions` mailing list.

## 7 Consent

The submitters hereby consent to all decisions of the CAESAR selection committee regarding the selection or non-selection of this submission as a second-round candidate, a third-round candidate, a finalist, a member of the final portfolio, or any other designation provided by the committee. The submitters understand that the committee will not comment on the algorithms, except that for each selected algorithm the committee will simply cite the previously published analyses that led to the selection of the algorithm. The submitters understand that the selection of some algorithms is not a negative comment regarding other algorithms, and that an excellent algorithm might fail to be selected simply because not enough analysis was available at the time of the committee decision. The submitters acknowledge that the committee decisions reflect the collective expert judgments of the committee members and are not subject to appeal. The submitters understand that if they disagree with published analyses then they are expected to promptly and publicly respond to those analyses, not to wait for subsequent committee decisions. The submitters understand that this statement is required as a condition of consideration of this submission by the CAESAR selection committee.

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## A TWINE Blockcipher [27]

We describe TWINE blockcipher by reusing materials from [27].

*Data Processing Part.* TWINE is a 64-bit blockcipher with 80 or 128-bit keys. We write TWINE-80 or TWINE-128 to denote the key length. We here focus on TWINE-80, which is used in CLOC. The global structure of TWINE is a variant of Type-2 generalized Feistel structure (GFS) with 16 nibbles (i.e. 4-bit sub-blocks). A round function of TWINE consists of a nonlinear layer using 4-bit S-boxes and a diffusion layer, which is a permutation on 16 nibbles. The diffusion layer of TWINE is not a cyclic shift and is chosen to provide a better diffusion property than the cyclic shift, according to the result of Suzaki and Minematsu [26]. This round function is iterated for 36 times for both key lengths, where the diffusion layer of the last round is omitted. For  $i = 1, \dots, 36$ , the  $i$ -th round uses a 32-bit round key,  $RK^i$ , which is derived from the 80-bit secret key,  $K$ , using the key schedule.

The data processing part essentially consists of a 4-bit S-box, denoted by  $S$ , and a permutation  $\pi$  over the indexes of 4-bit nibbles. That is, we have  $\pi : \{0, \dots, 15\} \rightarrow \{0, \dots, 15\}$ , where the  $j$ -th sub-block is mapped to the  $\pi[j]$ -th sub-block. Fig. 11 shows the encryption procedure, TWINE.Enc, and the decryption procedure, TWINE.Dec, using the derived round keys. Fig. 11 also shows S-box  $S$ , and the permutation  $\pi$  and its inverse. In all figures of this section, a variable  $X$  may have a subscript ( $i$ ) to express its length, i.e.,  $X$  may be written as  $X_{(|X|)}$ , for clearness. The round function is also illustrated in Fig. 13.

*Key Schedule Part.* The key schedule produces  $RK_{(32 \times 36)}$  from the 80-bit secret key  $K$ . It is also a variant of GFS with nibbles using the same S-box as data processing part. The key schedule uses 6-bit round constants,  $CON_{(6)}^i = CON_{H(3)}^i \parallel CON_{L(3)}^i$  for  $i = 1$  to 35. Fig. 12 shows the pseudocode of the key schedule, and Fig. 14 illustrates the key schedule for one round. In Fig. 12  $Rot_i(x)$  means  $i$ -bit left cyclic shift of  $x$ . We remark that  $CON^i$  corresponds to  $2^i$  in  $GF(2^6)$  with primitive polynomial  $z^6 + z + 1$ . The values of  $CON^i$  are also listed at Fig. 12.

We provide a test vector in Table 3.

<b>Algorithm</b> TWINE.Enc( $P_{(64)}, RK_{(32 \times 36)}, C_{(64)}$ )	<b>Algorithm</b> TWINE.Dec( $C_{(64)}, RK_{(32 \times 36)}, P_{(64)}$ )
<ol style="list-style-type: none"> <li>1. <math>X_{0(4)}^1 \parallel X_{1(4)}^1 \parallel \dots \parallel X_{15(4)}^1 \leftarrow P</math></li> <li>2. <math>RK_{(32)}^1 \parallel \dots \parallel RK_{(32)}^{36} \leftarrow RK_{(32 \times 36)}</math></li> <li>3. <b>for</b> <math>i = 1</math> <b>to</b> 35 <b>do</b></li> <li>4.   <math>RK_{0(4)}^i \parallel RK_{1(4)}^i \parallel \dots \parallel RK_{7(4)}^i \leftarrow RK_{(32)}^i</math></li> <li>5.   <b>for</b> <math>j = 0</math> <b>to</b> 7 <b>do</b></li> <li>6.     <math>X_{2j+1}^i \leftarrow S(X_{2j}^i \oplus RK_j^i) \oplus X_{2j+1}^i</math></li> <li>7.     <b>for</b> <math>h = 0</math> <b>to</b> 15 <b>do</b></li> <li>8.      <math>X_{\pi[h]}^{i+1} \leftarrow X_h^i</math></li> <li>9.     <b>for</b> <math>j = 0</math> <b>to</b> 7 <b>do</b></li> <li>10.      <math>X_{2j+1}^{36} \leftarrow S(X_{2j}^{36} \oplus RK_j^{36}) \oplus X_{2j+1}^{36}</math></li> <li>11. <math>C \leftarrow X_0^{36} \parallel X_1^{36} \parallel \dots \parallel X_{15}^{36}</math></li> </ol>	<ol style="list-style-type: none"> <li>1. <math>X_{0(4)}^{36} \parallel X_{1(4)}^{36} \parallel \dots \parallel X_{15(4)}^{36} \leftarrow C</math></li> <li>2. <math>RK_{(32)}^1 \parallel \dots \parallel RK_{(32)}^{36} \leftarrow RK_{(32 \times 36)}</math></li> <li>3. <b>for</b> <math>i = 36</math> <b>to</b> 2 <b>do</b></li> <li>4.   <math>RK_{0(4)}^i \parallel RK_{1(4)}^i \parallel \dots \parallel RK_{7(4)}^i \leftarrow RK_{(32)}^i</math></li> <li>5.   <b>for</b> <math>j = 0</math> <b>to</b> 7 <b>do</b></li> <li>6.     <math>X_{2j+1}^i \leftarrow S(X_{2j}^i \oplus RK_j^i) \oplus X_{2j+1}^i</math></li> <li>7.     <b>for</b> <math>h = 0</math> <b>to</b> 15 <b>do</b></li> <li>8.      <math>X_{\pi^{-1}[h]}^{i-1} \leftarrow X_h^i</math></li> <li>9.     <b>for</b> <math>j = 0</math> <b>to</b> 7 <b>do</b></li> <li>10.      <math>X_{2j+1}^1 \leftarrow S(X_{2j}^1 \oplus RK_j^1) \oplus X_{2j+1}^1</math></li> <li>11. <math>P \leftarrow X_0^1 \parallel X_1^1 \parallel \dots \parallel X_{15}^1</math></li> </ol>

$x$	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	C	0	F	A	2	B	9	5	8	3	D	7	1	E	6	4

$h$	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
$\pi[h]$	5	0	1	4	7	12	3	8	13	6	9	2	15	10	11	14
$\pi^{-1}[h]$	1	2	11	6	3	0	9	4	7	10	13	14	5	8	15	12

**Fig. 11.** Data processing part of TWINE (top) with S-box  $S$  (middle) and permutation  $\pi$  (bottom)

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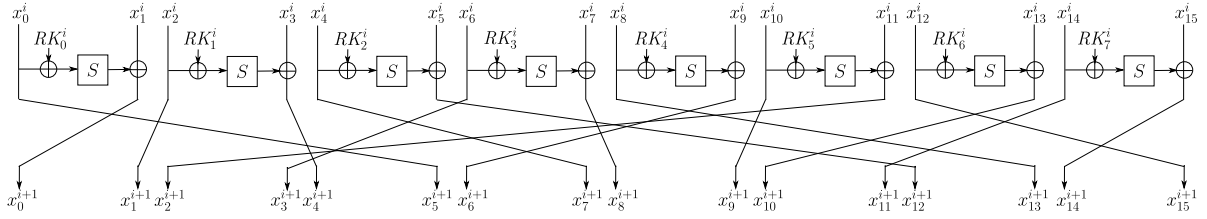
**Algorithm TWINE.KeySchedule-80( $K_{(80)}, RK_{(32 \times 36)}$ )**


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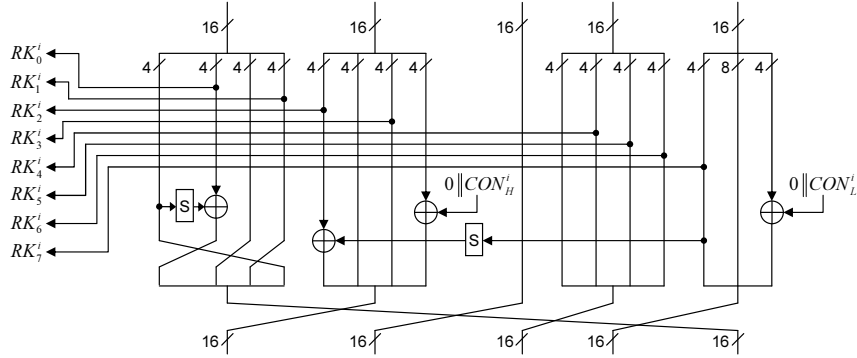
1.  $WK_{0(4)} \| WK_{1(4)} \| \dots \| WK_{19(4)} \leftarrow K$
  2. **for**  $r = 1$  **to** 35 **do**
  3.  $RK_{(32)}^r \leftarrow WK_1 \| WK_3 \| WK_4 \| WK_6 \| WK_{13} \| WK_{14} \| WK_{15} \| WK_{16}$
  4.  $WK_1 \leftarrow WK_1 \oplus S(WK_0)$
  5.  $WK_4 \leftarrow WK_4 \oplus S(WK_{16})$
  6.  $WK_7 \leftarrow WK_7 \oplus 0 \| CON_H^r$
  7.  $WK_{19} \leftarrow WK_{19} \oplus 0 \| CON_L^r$
  8.  $WK_0 \| \dots \| WK_3 \leftarrow \text{Rot4}(WK_0 \| \dots \| WK_3)$
  9.  $WK_0 \| \dots \| WK_{19} \leftarrow \text{Rot16}(WK_0 \| \dots \| WK_{19})$
  10.  $RK_{(32)}^{36} \leftarrow WK_1 \| WK_3 \| WK_4 \| WK_6 \| WK_{13} \| WK_{14} \| WK_{15} \| WK_{16}$
  11.  $RK \leftarrow RK^1 \| RK^2 \| \dots \| RK^{36}$
- 

$i$	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
$CON^r$	01	02	04	08	10	20	03	06	0C	18	30	23	05	0A	14	28	13	26
$i$	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	
$CON^r$	0F	1E	3C	3B	35	29	11	22	07	0E	1C	38	33	25	09	12	24	

**Fig. 12.** Key schedule of TWINE-80. S-box  $S$  is the same as Fig. 11.



**Fig. 13.** Round function of TWINE



**Fig. 14.** 80-bit key schedule

**Table 3.** A test vector of TWINE-80 in hexadecimal notation

key (80 bits)	00112233 44556677 8899
plaintext	01234567 89ABCDEF
ciphertext	7C1F0F80 B1DF9C28